1 2 3 4 5 6 7 a	TOWNSEND AND TOWNSEND AND CRIERIC P. JACOBS (State Bar No. 88413) PETER H. GOLDSMITH (State Bar No. 912 ROBERT A. McFARLANE (State Bar No. 1 IGOR SHOIKET (State Bar No. 190066) Two Embarcadero Center, 8th Floor San Francisco, California 94111 Telephone: (415) 576-0200 Facsimile: (415) 576-0300 E-mail: epjacobs@townsend.com	94) 72650)	
8 9	Attorneys for Defendant and Counterclaimant FAIRCHILD SEMICONDUCTOR CORPORATION		
10	UNITED STATES DISTRICT COURT		
11	FOR THE NORTHERN DISTRICT OF CALIFORNIA		
12	SAN FRANCISCO DIVISION		
13			
14 15 16 17 18 19 20 21 22 23	ALPHA & OMEGA SEMICONDUCTOR, INC., a California corporation; and ALPHA & OMEGA SEMICONDUCTOR, LTD., a Bermuda corporation, Plaintiffs and Counterdefendants, v. FAIRCHILD SEMICONDUCTOR CORP., a Delaware corporation, Defendant and Counterclaimant. AND RELATED COUNTERCLAIMS.	(Consolida [PROPOS PLAINTII RESPONS	E 07-2638 JSW (EDL) ted with Case No. C 07-2664 JSW) ED] ORDER DENYING FFS' MOTION TO COMPEL SES TO INTERROGATORIES DUCTION OF DOCUMENTS December 18, 2007 9:00 a.m. Courtroom E, 15th Floor Hon. Elizabeth D. Laporte
23	AND RELATED COUNTERCLAIMS.		
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Plaintiff's Motion to Compel Responses to Interrogatories and Production of Documents was			
heard before this Court on December 18, 2007 The Court, having read and considered the supporting			
and opposing papers, the court file in this case and all other matters presented to the Court, and having			
considered the arguments of counsel, and good cause appearing therefor, hereby ORDERS:			
Plaintiff's Motion to Compel Responses to Interrogatories and Production of Documents is			
DENIED.			
The Court adopts the following definition of "Accused Fairchild Device" for the purposes of			
discovery requests:			
The term "Accused Fairchild Device" shall mean any device which includes a power MOSFET made, used, offered for sale, or imported into the United States by Fairchild that comprises one or more of the following: (a) any IC that includes a source contact area that is divided by at least one gate runner into two or more subcontact areas, and wherein each of the subcontact areas is connected to a lead-frame by more than one lead-wire; and/or (b) devices made by a method in which a body region is formed adjacent to a trench, a source region is formed in the body region, and an implant is made into the body region of the same conductivity type as the source and which is other than an implant used to form the source; and/or (c) MOSFET transistors having a body region formed through three dopant implants.			
Dated: Honorable Elizabeth D. Laporte			
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